

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/644,225	KELLER, IGOR	
		Examiner	Art Unit	Page 1 of 1
		Andre Pierre-Louis	2123	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title, Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Lim et al., Accurate Worst Case Timing Analysis Techniques for RISC Processor, IEEE 1994.
	V	Malik et al., Static Timing Analysis of Embedded Software, 1997.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.